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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/434,654	11/05/1999	KEVIN J RYAN	303.306US4	4209
7:	590 01/05/2004		EXAM	INER
SCHWEGMAN LUNDBERG WOESSNER & KLUTH PA			PEIKARI, BEHZAD	
P O BOX 2938 MINNEAPOLI	S, MN 55402		ART UNIT PAPER NUMBER	
	,		2186	20
			DATE MAILED: 01/05/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
Advisory Action	09/434,654	RYAN, KEVIN J	
Advisory Addion	Examiner	Art Unit	
	B. James Peikari	2186	
The MAILING DATE of this communication appe	ears on the cover sheet with the c	orrespondence add	ress
THE REPLY FILED 04 December 2003 FAILS TO PLACE Therefore, further action by the applicant is required to avainal rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appea Examination (RCE) in compliance with 37 CFR 1.114.	oid abandonment of this applica) a timely filed amendment whicl I (with appeal fee); or (3) a timel	ation. A proper repl n places the applica	y to a ition in
PERIOD FOR RE	EPLY [check either a) or b)]		
a) The period for reply expires 4 months from the mailing date b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire I ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The ee have been filed is the date for purposes of determining the period of ee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of 2) as set forth in (b) above, if checked. Any reply received by the Officimely filed, may reduce any earned patent term adjustment. See 37 CFR	Advisory Action, or (2) the date set forth ater than SIX MONTHS from the mailing FILED WITHIN TWO MONTHS OF The date on which the petition under 37 CF of extension and the corresponding amount the shortened statutory period for reply ce later than three months after the mail	g date of the final rejecting FINAL REJECTION. R 1.136(a) and the approperture of the fee. The appropriationally set in the final	on. See MPEP opriate extension ropriate extension Office action; or
 A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFF 			
2. The proposed amendment(s) will not be entered be	ecause:		
(a) They raise new issues that would require further	er consideration and/or search (s	see NOTE below);	
(b) they raise the issue of new matter (see Note b	elow);		
(c) they are not deemed to place the application in issues for appeal; and/or	n better form for appeal by mate	rially reducing or sir	mplifying the
(d) they present additional claims without canceling NOTE:	ng a corresponding number of fi	nally rejected claim	S
3. Applicant's reply has overcome the following reject	ion(s):		
 Newly proposed or amended claim(s) would canceling the non-allowable claim(s). 	be allowable if submitted in a se	eparate, timely filed	amendment
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for application in condition for allowance because: Se		dered but does NO	T place the
6. The affidavit or exhibit will NOT be considered becraised by the Examiner in the final rejection.	ause it is not directed SOLELY to	o issues which were	e newly
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we			and an
The status of the claim(s) is (or will be) as follows:			
Claim(s) allowed: Claim(s) objected to: Claim(s) rejected: <u>1-4 and 32-60</u> . Claim(s) withdrawn from consideration:	·		
8.☐ The drawing correction filed on is a)☐ appr	roved or b) disapproved by the	ne Examiner.	
9. Note the attached Information Disclosure Statemer	nt(s)(PTO-1449) Paper No(s)	·	
0. Other:		el.	
		wice_	
	·	B. James Peikari Primary Examiner Art Unit: 2186	

Continuation of 5. does NOT place the application in condition for allowance because: Briefly, (a) with regard to the first point on page 13 this is most since claim 1 was cancelled, it is further noted that in circuit diagrams a line with one arrow is unidirectional and a line with arrows on both ends is bidirectional, with this in mind, applicant will note that the buses cited in the rejection explicitly teach the claimed buses; (b) with regard to the second point on page 13, just as the memory modules are duplicated, so are the associated registers ... has applicant assumed that the plural modules of the cited combination would share a single register??, (c) with regard to the third point on page 13 and the discussion of the St. Regis case law on page 14, applicant appears to consider the reduction of load a novel and unexpected feature, however this is a very well known feature of parallel memory hiearchies. In fact, that is exactly what applicant is claiming — a parallel memory hierarchy. Such systems combined the benefits of parallel storage (reduced load and access to plural devices) with the benefits of memory hiearchies (faster retrieval of data). There is nothing new about this.

As for the claimed connections of devices in claim 34, discussed on page 15 and for the similar claims, each of these elements was specifically taught having the same connectivity as the claims, element by element, in the rejection. Consequently applicant's arguments are not understood.

Also, as for the use of packets and a corresponding packet protocol -- packets were simply units of data to be transferred. Any data processing system had an established protocol for transferring data. In any case, the transfer of packets in and out of DRAMs was explicitly mentioned by Katayama et al., note column 2, lines 37-43.